

De-Embedding with Scalable Groundshield Teststructures

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- Overview on Calibration/De-Embedding
- Groundshields
- Why is scaling important?
- Results
- Work to do
- References
- and finally . . .



For Si-based devices RF-characterization is typically done by :

- 1. Calibrating the NWA up to the probe tips on calibration substrates available from several vendors, using one of several well-known methods like SOLT, TRL, LRM, . . .
- 2. Removing surplus parasitics, not belonging to the device proper, thru de-embedding, using one-way to four-way methods [1, 2, 3, 4].

Because of lacking accuracy Si-based calibration standards are **not** normally used.



Why Groundshields?

Groundshields -

- were first used to create low-noise pads for LNA design [5].
- were used for teststructures in low substrate resistivity CMOS processes [6].
- their objective is to remove substrate coupling between signal leads.
- use the lower metal level(s) for shielding.
- tend to increase parallel capacitance



Comparison with std open

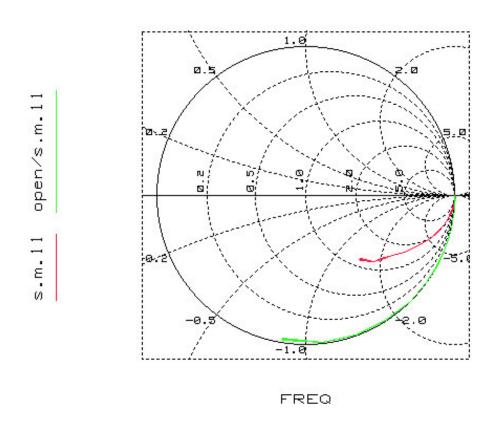


Figure 1: scattering parameter s_{11} of standard and groundshield open



Groundshield geometry

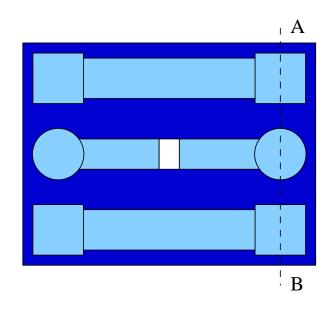


Figure 2: top view showing groundshield with extension, metallization and DUT gap

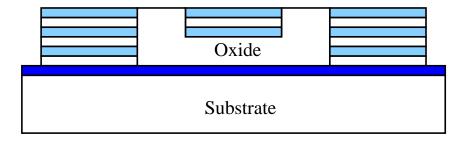


Figure 3: cross section along points A, B of fi gure 1



Why is scaling important?

De-embedding different sized DUT's can be done by :

- Keeping the length of the leads constant. This implies moving the pads and cabling with DUT size.
- Keeping the pad positions fixed. This implies changing the length of the leads.
- Depending on the method used, up to 4x chip space is needed for de-embedding structures.
- Often accuracy is sacrificed by using only a subset of the required de-embedding structures.

Answer: economical & accuracy reasons



Scaling experiment

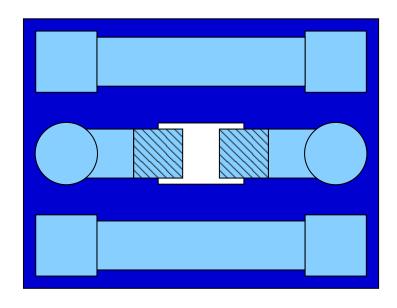
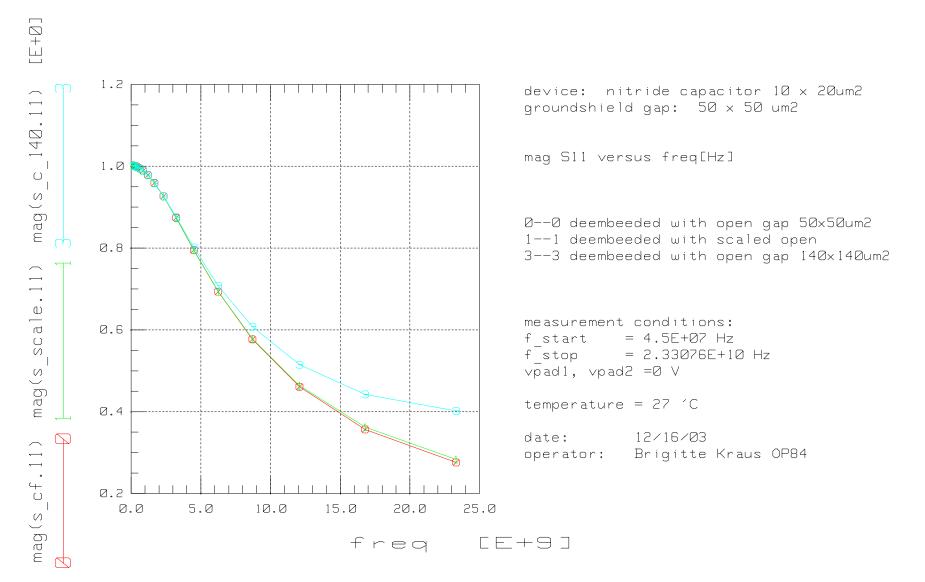


Figure 4: lead length is scaled, (crosshatched area)

DUT: Nitride capacitor array of 5 different sizes with correponding open de-embedding structures

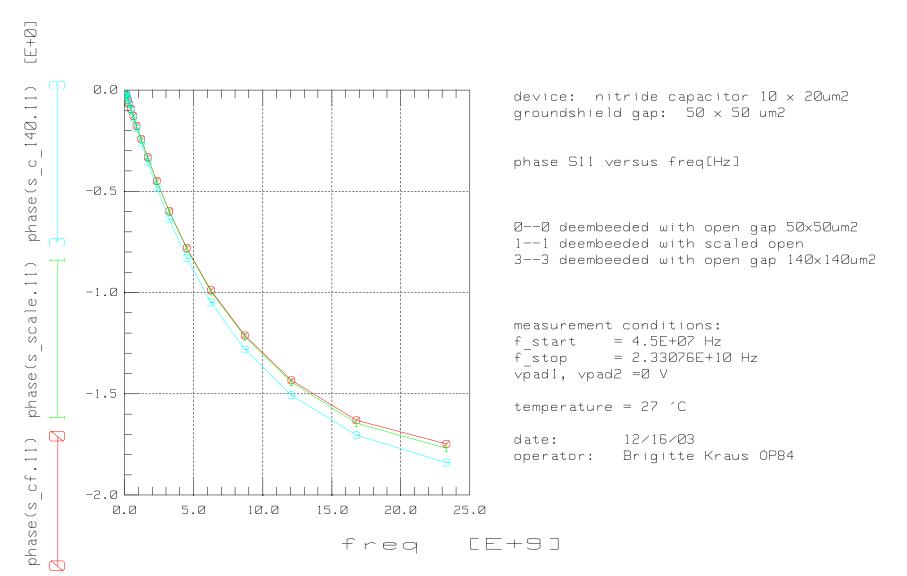






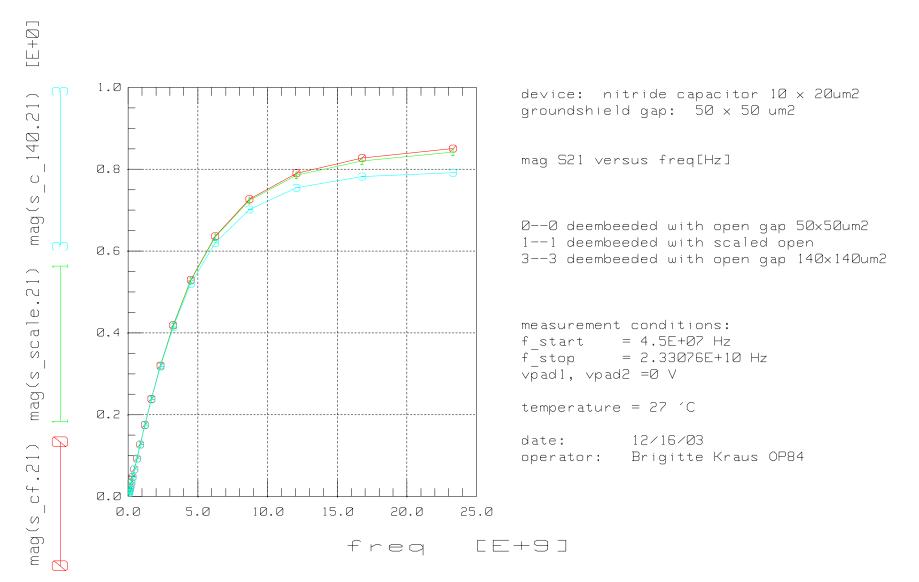






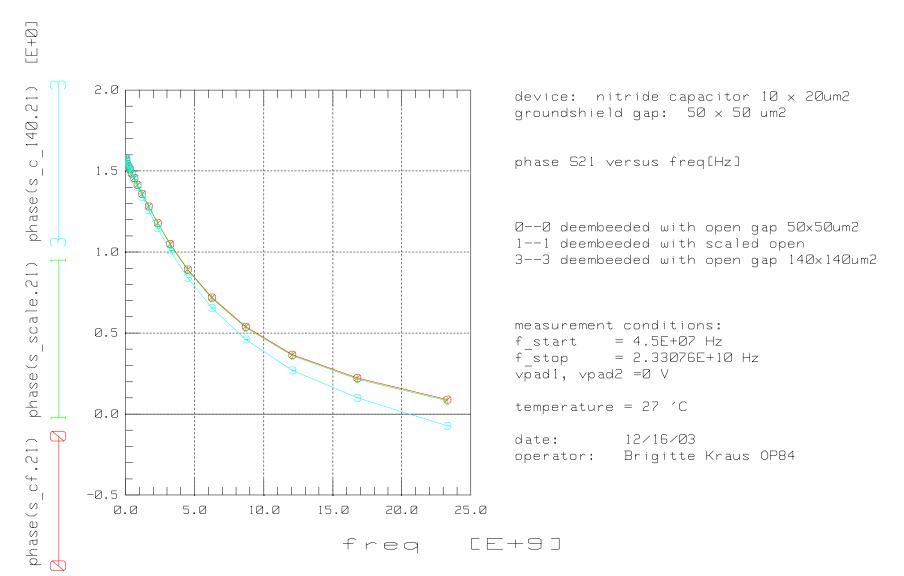






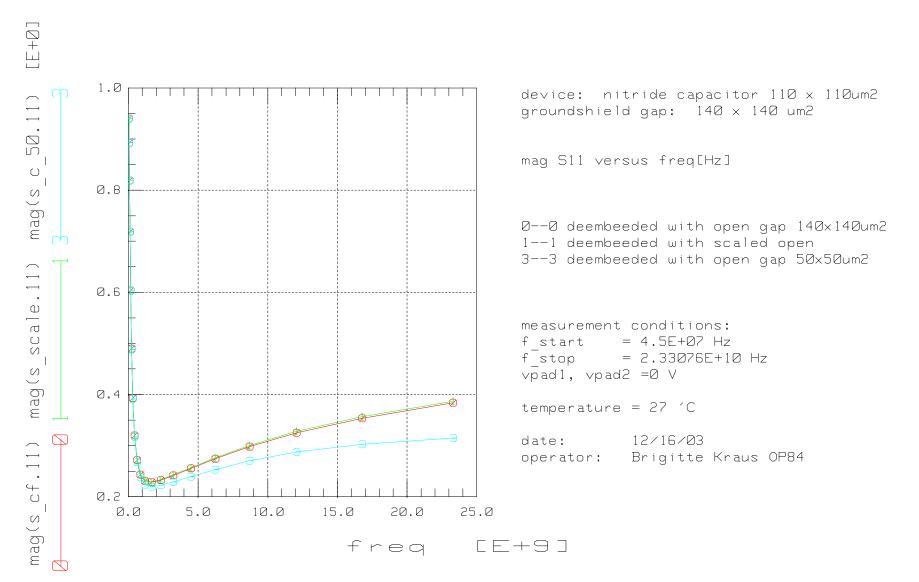






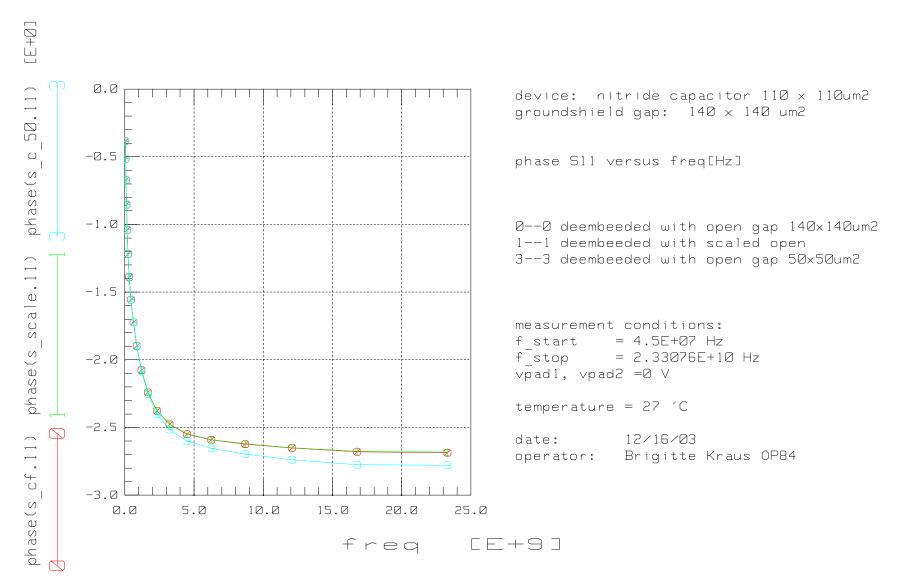






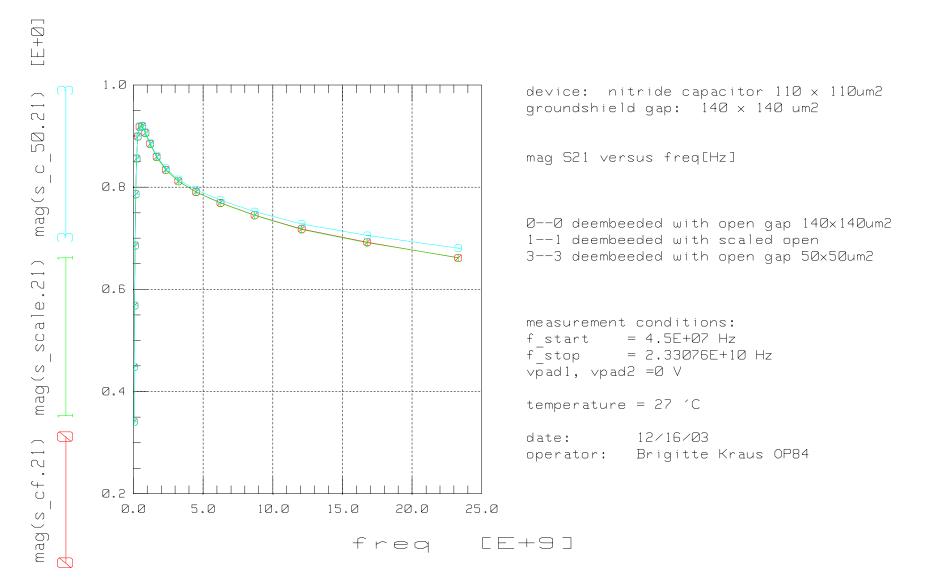






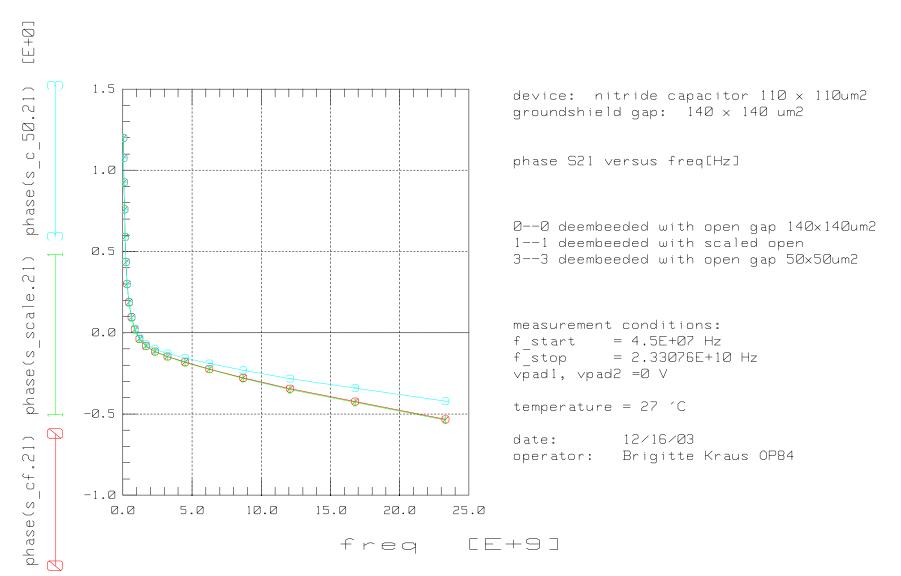
















- Comparison of standard and groundshielded open structure was shown.
- Scalability of open structure was demonstrated.
- Economical impact was discussed.
- Characterization of full-blown testchip inc. structures for 3-way de-embedding and SOLT calibration is underway.





References

- [1] P.J. van Wijnen et.al., BCTM, 1987
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- [4] T.E. Kolding, IEEE Trans. Electron Devices, Apr 2000
- [5] J.T. Colvin, et.al., BCTM, 1999
- [6] T.E. Kolding *et.al.*, *ICMTS*, 2000





Thank you!