



3D SELF HEATING MODELING FOR ELECTRO-THERMAL CHARACTERISATION OF SiGe HBTs

P-Y. SULIMA, J-L BATTAGLIA, T. ZIMMER, H. BEKRICH, D. CELI

5th European HICUM Workshop
STMicronics Crolles, France June 6-7, 2005

Outline

- I. Introduction
- II. Modeling
- III. Measurement verification
- IV. Conclusions



Introduction

SiGe heterojunction bipolar transistors (HBT SiGe) technologies are of great importance for the semiconductor market to produce high speed devices :

- Low cost and mature Technology
- High RF performances (F_t , F_{tmax} , current gain)
- Low noise



High frequency performance
High current density

➤ High power effect : self heating

➤ Modification of the electrical parameters ($I_B \uparrow$) $V_{BE} \downarrow$... performances drop



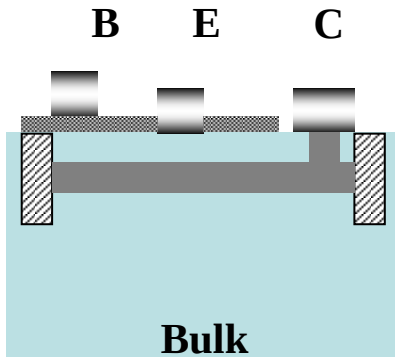
Work

Better understanding and Modeling electrical devices : HBT SiGe

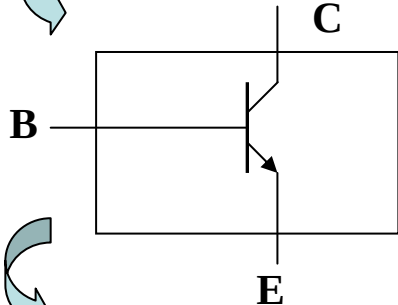


Modeling

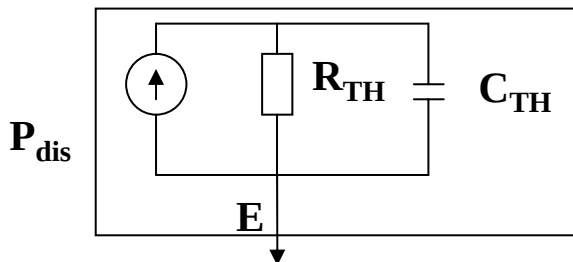
Geometrical structure NPN



Electrical représentation



THERMAL Network



Compact Models (Mextram, VBIC, HICUM) based on the equation [1]

$$\rho c \frac{\partial T(M, t)}{\partial t} = \nabla \cdot (\lambda(T(M, t)) \nabla T(M, t)) + H$$

$T(M, t)$ = Temperature [K]

$\lambda(T(M, t))$ = Heat conductivity [$\text{Wm}^{-1}\text{K}^{-1}$]

c = Heat capacity [$\text{Jkg}^{-1}\text{K}^{-1}$]

ρ = Density [kgm^{-3}]

H = Heat source : intrinsic transistor (Joule heat of phonons, electrons and holes, recombination heat, Peltier Thomson heat – Thermoelectric powers variation , radiation)

Thermal network sub circuit

$$R_{TH} C_{TH} \frac{\partial T_j}{\partial t} = -(T_j - T_{amb}) + R_{TH} P_{dis}(t)$$

$$P_{dis}(t) \approx i_c(t) \times v_{ce}(t)$$

Analytical solution

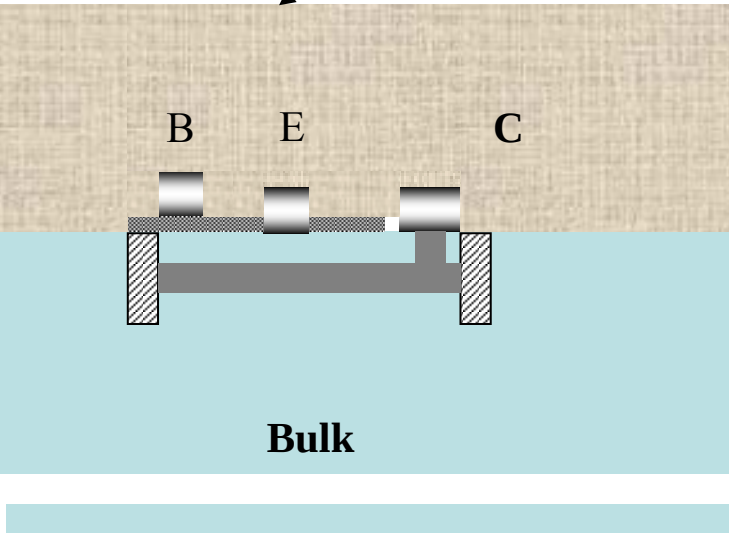
$$(T_j - T_{amb}) = R_{TH} P_{dis} \left[1 - \exp\left(\frac{-t}{R_{TH} C_{TH}}\right) \right]$$

[1] – G. K. Wachutka – “Rigorous thermodynamic treatment of heat generation and conduction in semiconductor device modeling” – IEEE Transactions on Computer Aided Design, Vol.9, N°11, November 1990

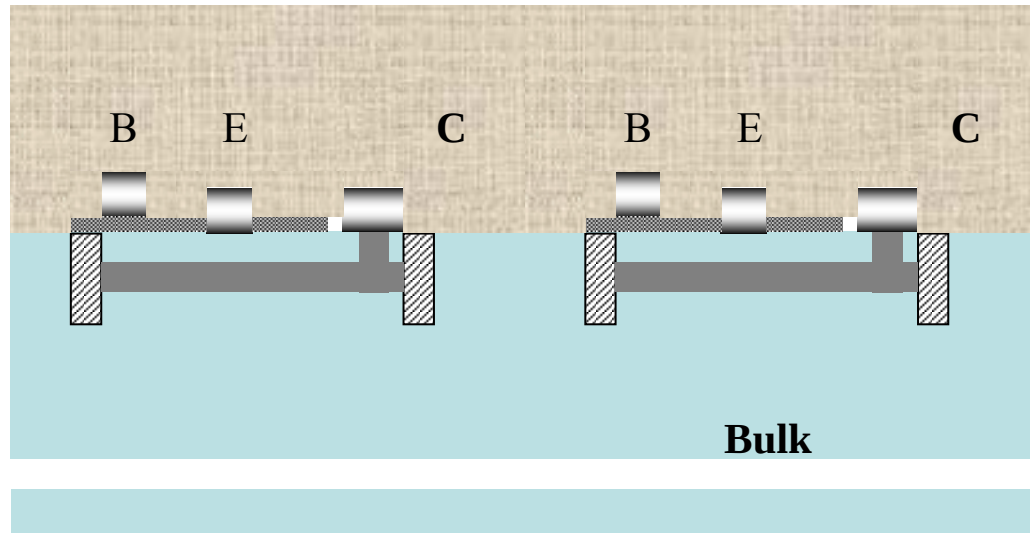
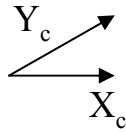
Modeling

Compact Model

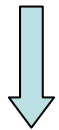
Upper layers



Periodicity



Experiment : X_c and $Y_c \uparrow$

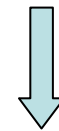


Model validation

R_{TH}

C_{TH}

Real geometry : X_c and Y_c limited

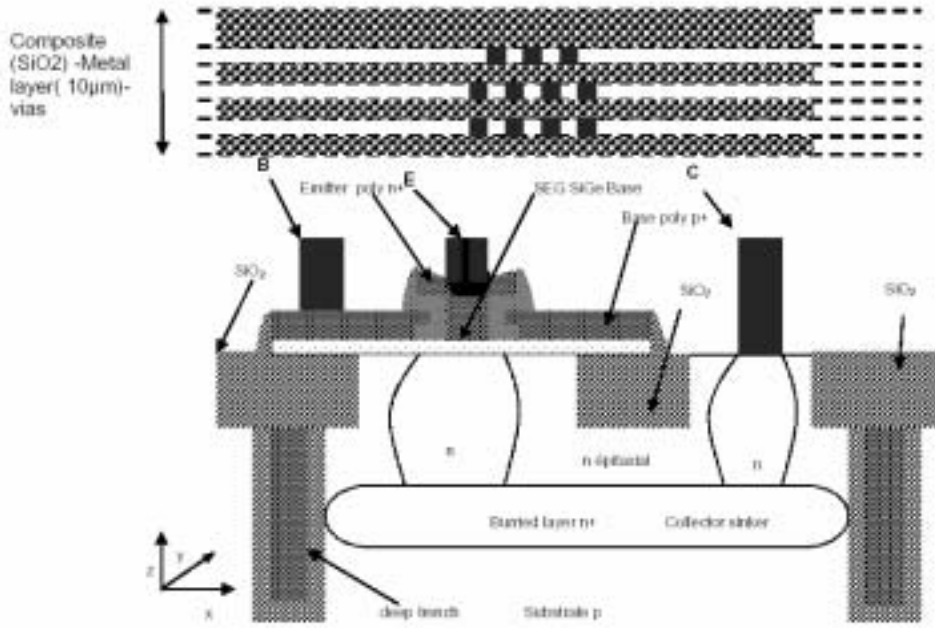


Simulation

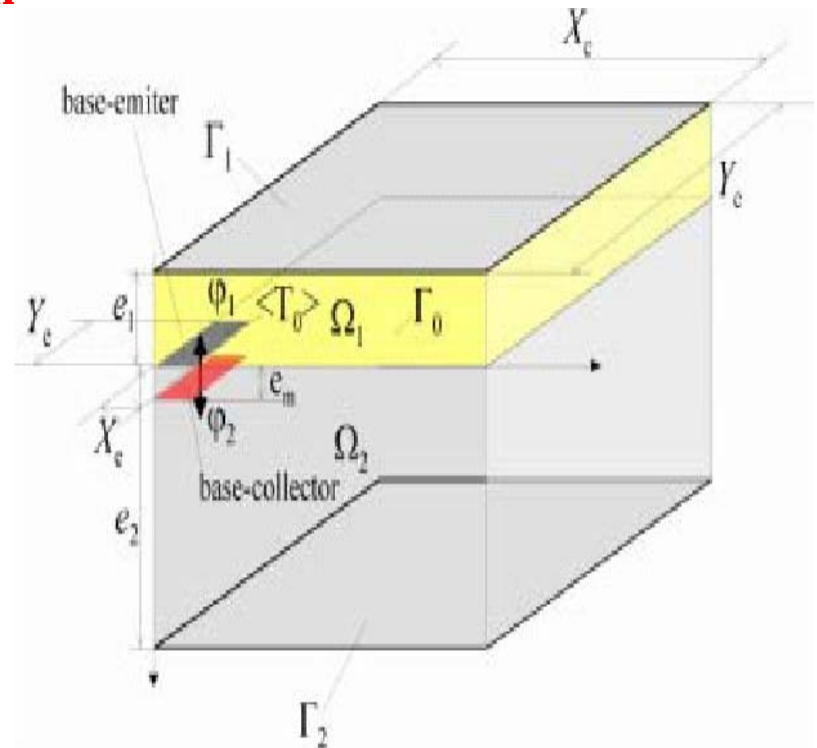


Modeling

Compact Model



Schematic cross section of the HBT SiGe



Transistor representation

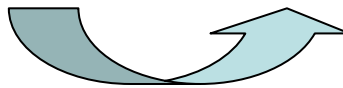
Layer 1

Composite SiO₂ metal homogenized

Layer 2

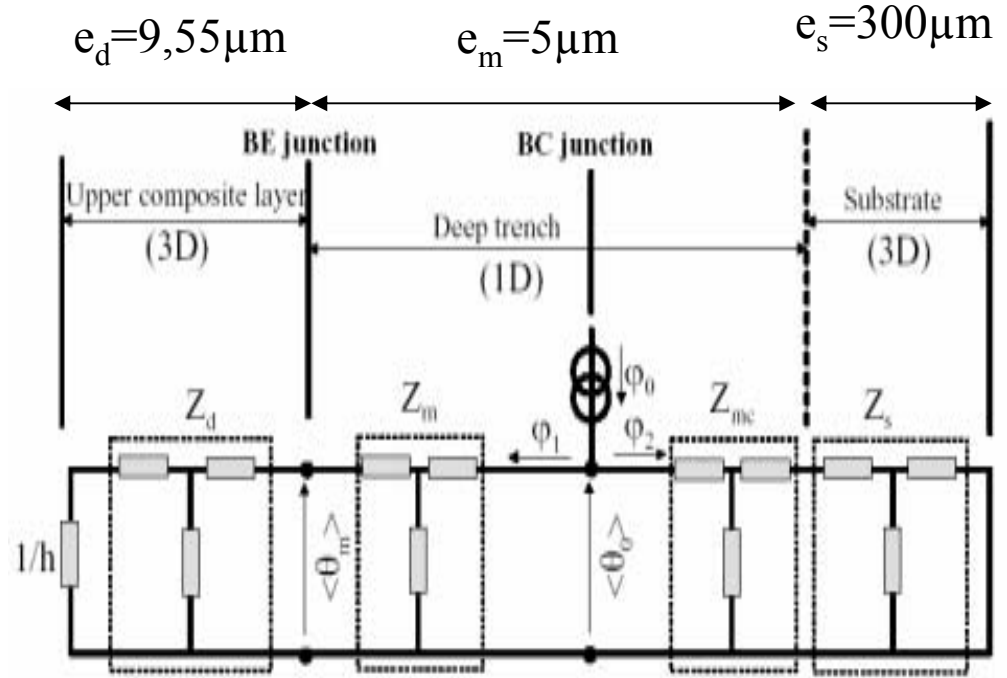
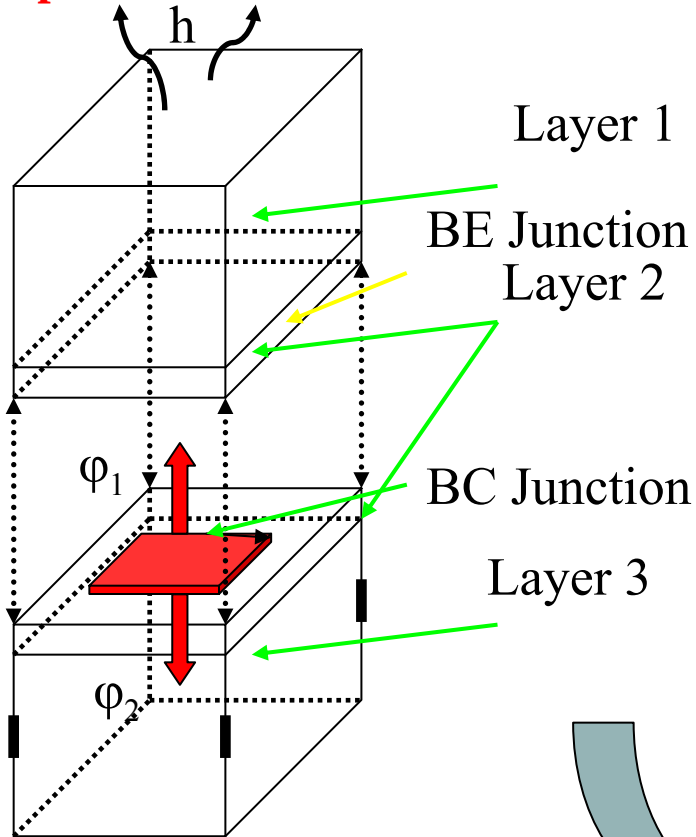
Deep trench and active transistor

Layer 3 Substrate



Modeling

Compact Model



HBT SiGe thermal network

Composite SiO₂ metal homogenized parameters

$$\lambda_d = f(e_d, \frac{e_{Al}}{\lambda_{Al}}, \frac{e_W}{\lambda_W}, \frac{e_{SiO_2}}{\lambda_{SiO_2}})$$

$$(\rho C_p)_d = g(e_d, (\rho C_p)_{Al} e_{Al}, (\rho C_p)_W e_W, (\rho C_p)_{SiO_2} e_{SiO_2})$$

Transistor representation

Layer 1

Composite SiO₂ metal homogenized

Layer 2

Deep trench and active transistor

Layer 3 Substrate

Modeling

Compact Model

General resolution

3D Heat diffusion equation

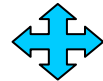


Heat generated BC junction H Heaviside function

$$\varphi = \varphi_0(t) [H(x) - H(x - X_e)] [H(y) - H(y - Y_e)]$$



Initial condition



Boundaries conditions



Spatial periodicity
 $x=0, y=0; x=X_c, y=Y_c$

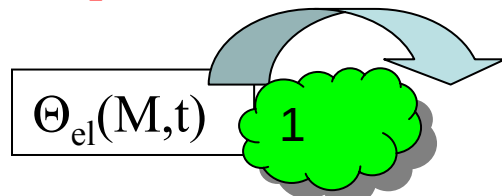


Analytical solution

Modeling

Compact Model

Analytical solution



Laplace
+initial condition
+boundaries condition



Integral Fourier Transform x
Integral Fourier Transform y

$$\langle \Theta_{el}(x, y, z, t) \rangle$$



Inverse Laplace
transform (Stehfest
algorithm)



Inverse Fourier
transform x and y
And average temperature



Quadruples formalism

$$Z_{TH}(\alpha_n, \beta_m, z, p)$$



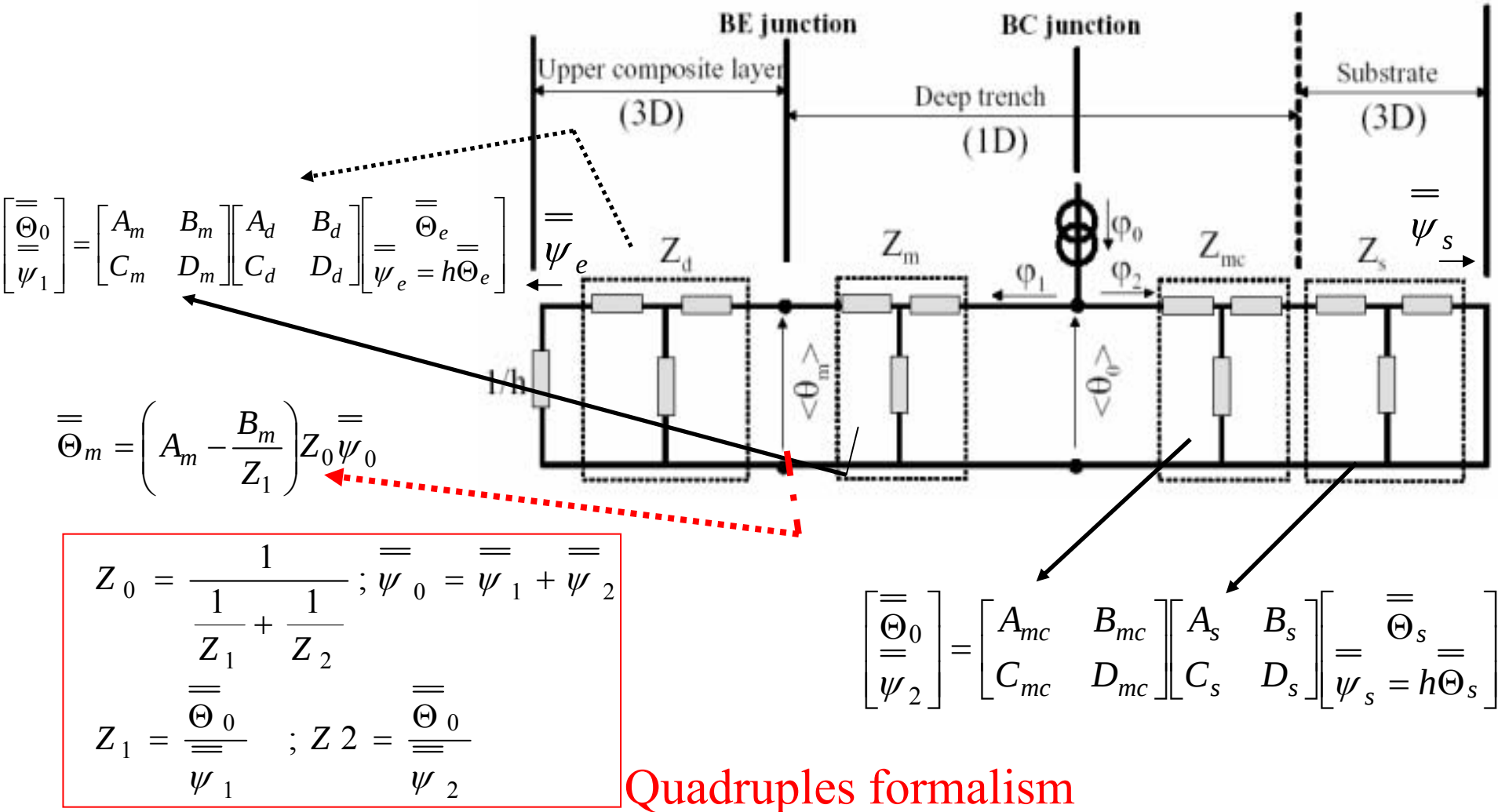
Asymptotic value R_{TH}, C_{TH}

$$\bar{\Theta}_{el}(\alpha_n, \beta_m, z, p)$$



Modeling

Compact Model



Quadruples formalism



Modeling

Compact Model

Asymptotic behavior

$$\overline{\overline{\Theta}}_m = \left(A_m - \frac{B_m}{Z_1} \right) Z_0 \overline{\overline{\psi}}_0$$

$$Z_{TH}(p) = \left(A_m - \frac{B_m}{Z_1} \right) Z_0$$

Asymptotic thermal resistance

$$Z_{TH}(p \rightarrow 0) \rightarrow R_{TH}$$

Asymptotic thermal capacitance

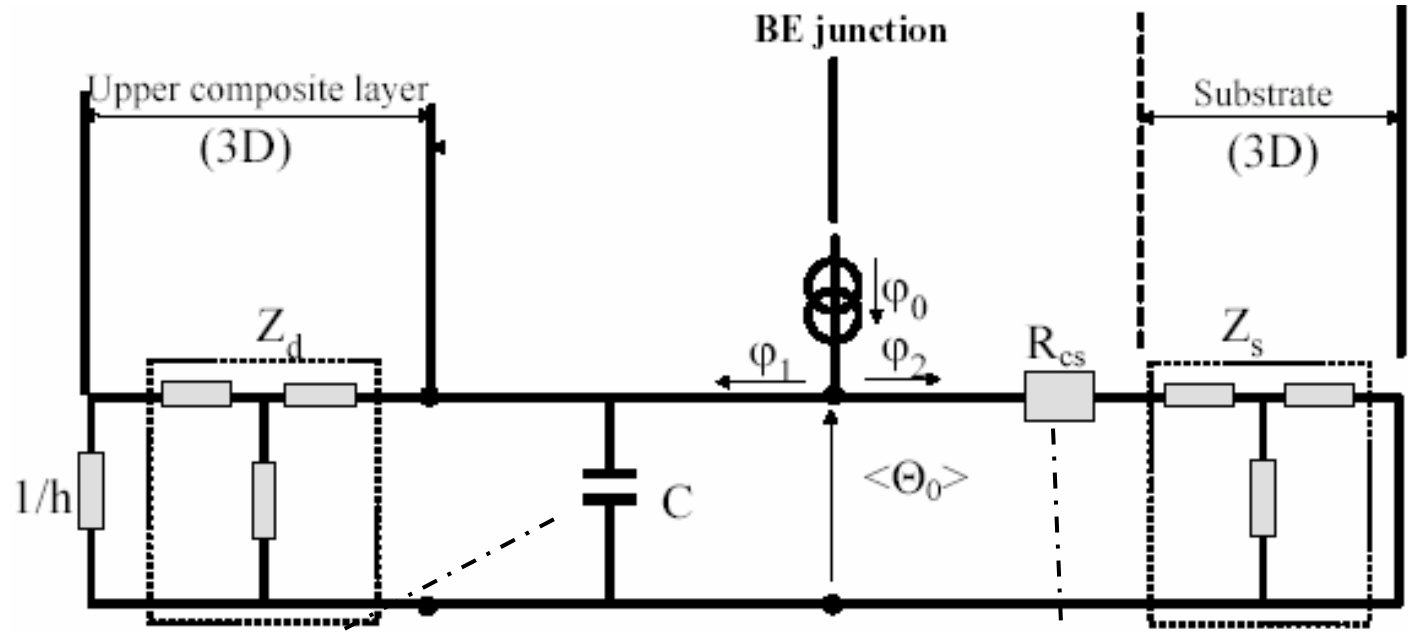
$$Z_{TH}(p \rightarrow \infty) \rightarrow C_{TH}$$



Modeling

Compact Model

Volume source assumption



$$C = h((\rho C)_m, e_m, \frac{S_D}{S_e}, S_e) \quad \text{Volume source term}$$

Asymptotic thermal resistance

$$Z_{TH}(p \rightarrow 0) \rightarrow R_{TH}$$

$$R_{cs} = \frac{e_m}{\lambda_m}$$

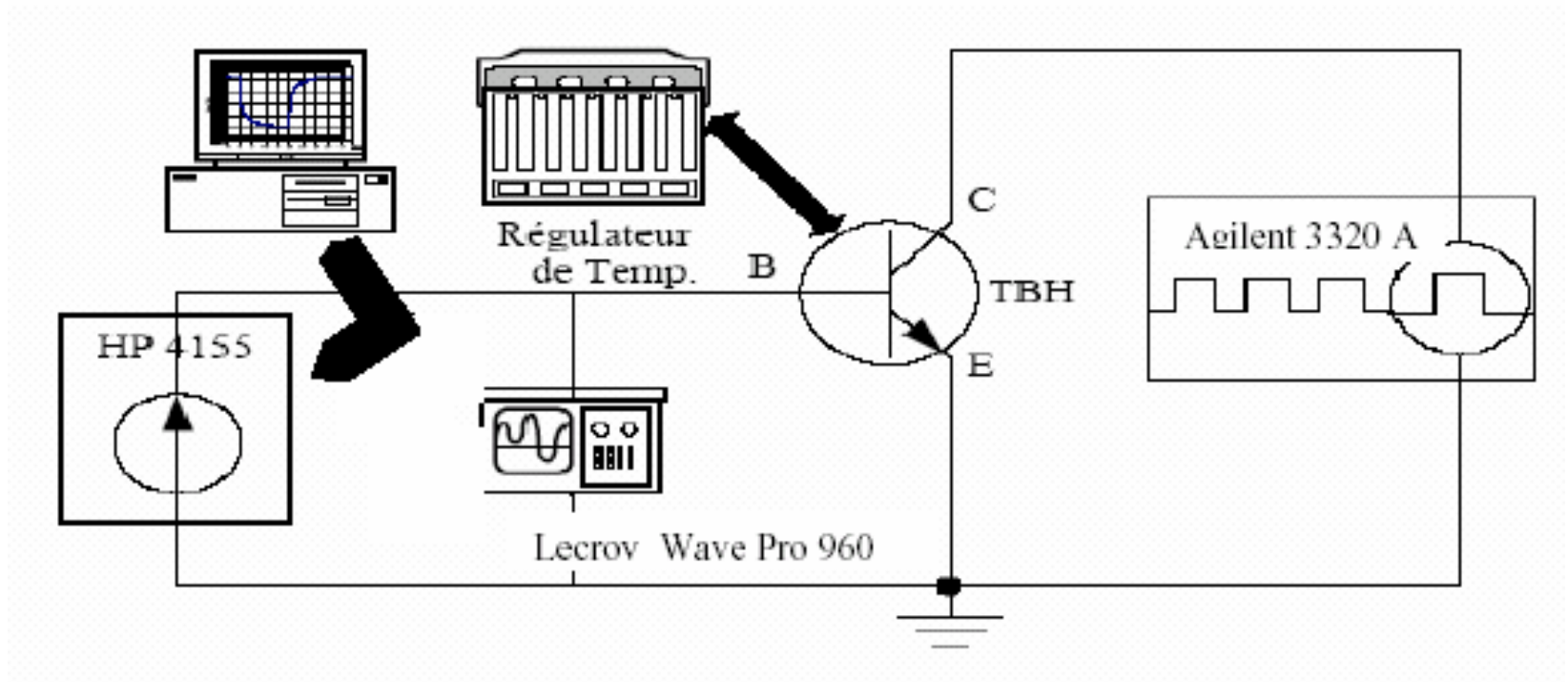
Asymptotic thermal capacitance

$$Z_{TH}(p \rightarrow \infty) \rightarrow C_{TH} = R_{TH} \times (\sqrt{\lambda_d(\rho C)_d} + \sqrt{\lambda_s(\rho C)_s})^2 + C$$



Measurements verification

Five steps experimental verification



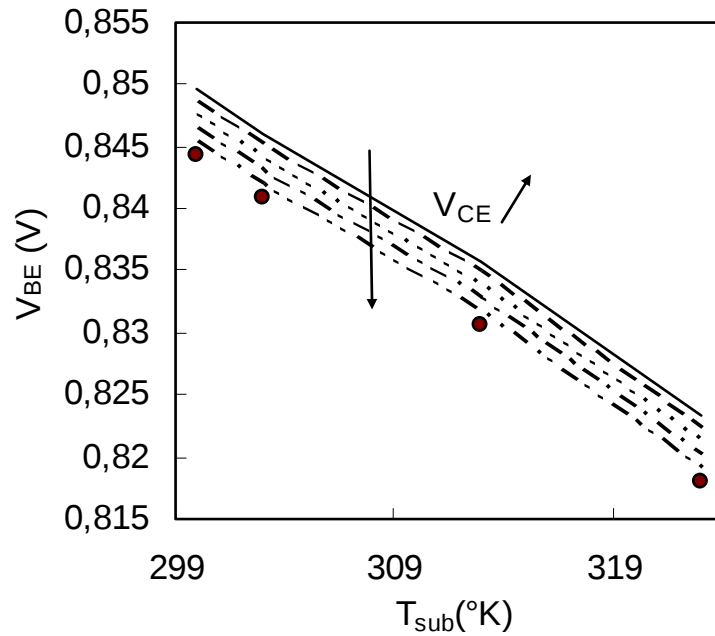
General measurement set up



Measurements verification

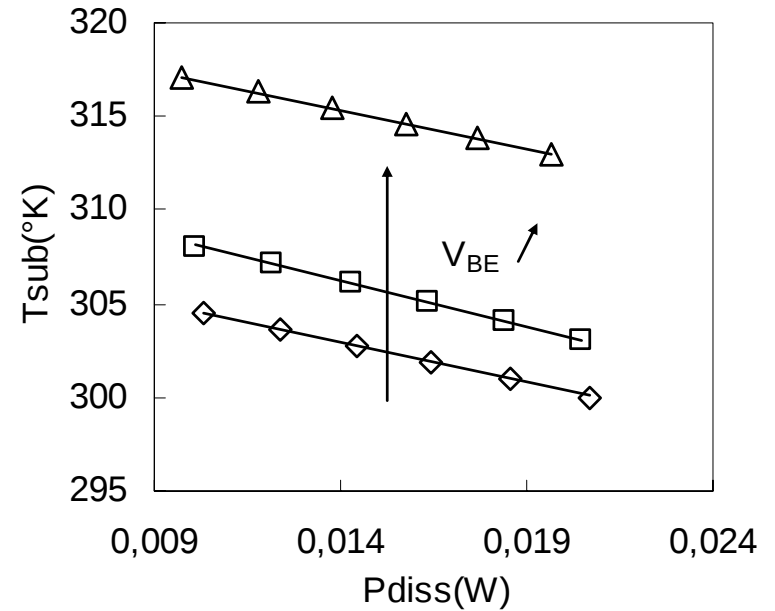
DC

1



Calibration
 $V_{BE} = f(T_i)$
 $A_e = \text{cte} ; I_b = c^{te}$

2



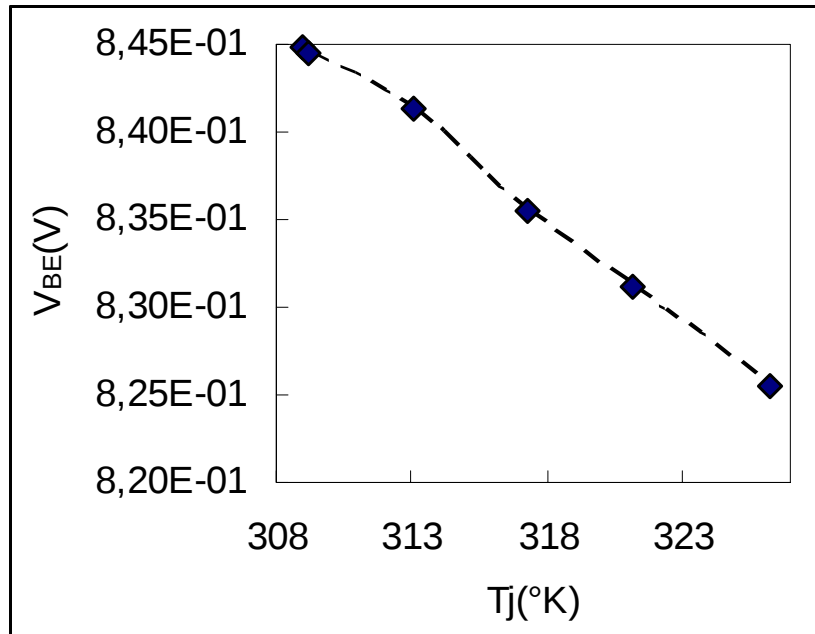
$T_{sub} = f(P_{diss})$
 $A_e = \text{cte} ; V_{BE} = c^{te} ; I_b = c^{te}$



Measurements verification

DC

3

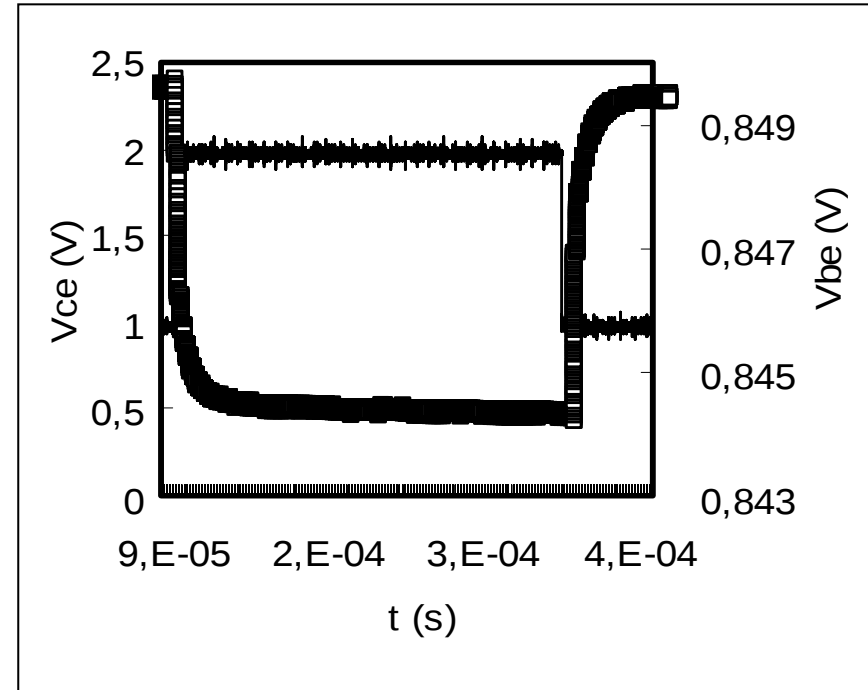


$$V_{BE}=f(T_j)$$

$$A_e=cte ; V_{BE}=c^{te} ; I_b=c^{te}$$

Transient

4



Measured

$$V_{be}=f(t)$$

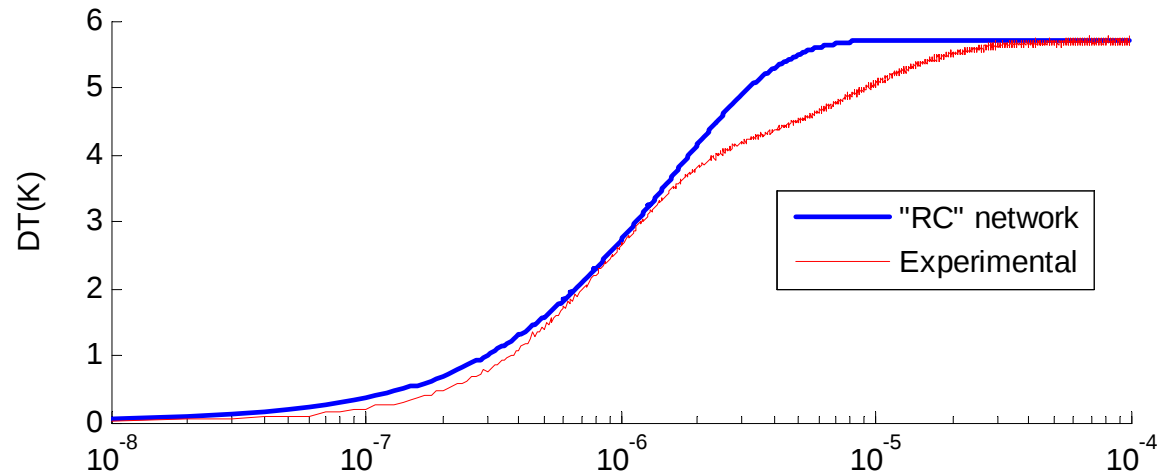
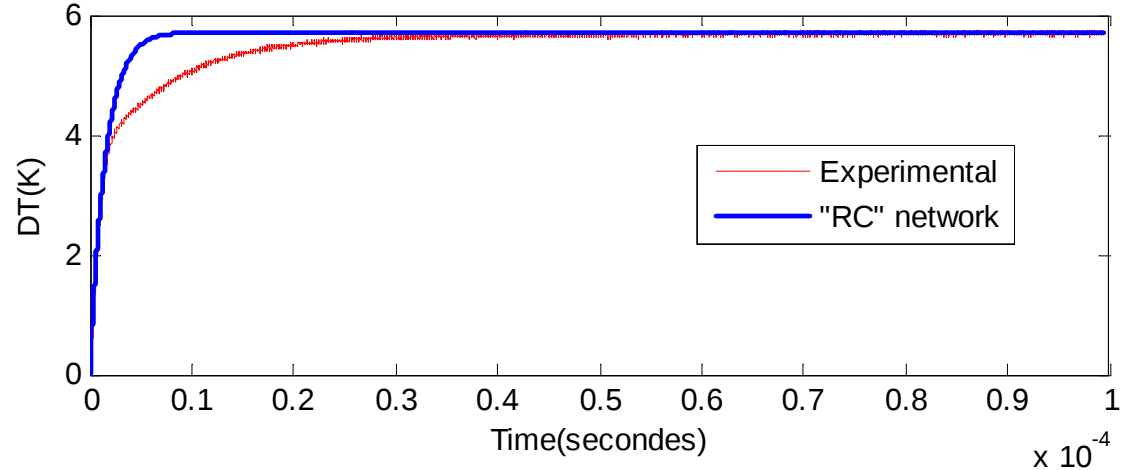
$$A_e=cte ; V_{CE}=pulse[1;2]volts ; I_b=c^{te}$$



Measurements verification

Transient

5



$$DT = P_{dis} \times R_{TH} \left(1 - e^{-\frac{t}{R_{TH} C_{TH}}} \right)$$

Thermogram : $T_j = f(t)$; $A_e = 6,56 \mu m^2$

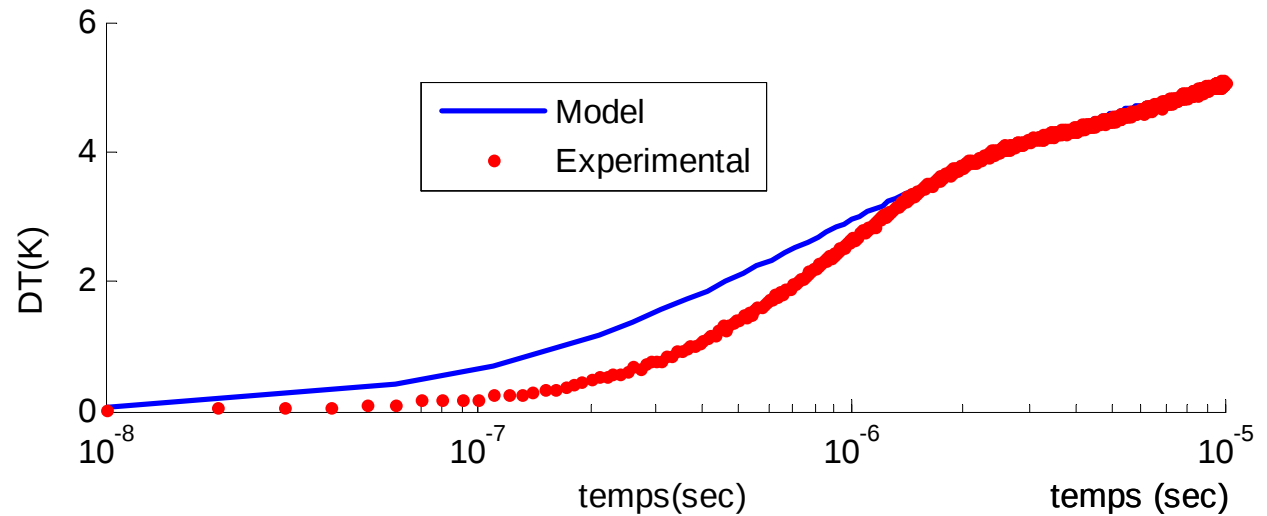
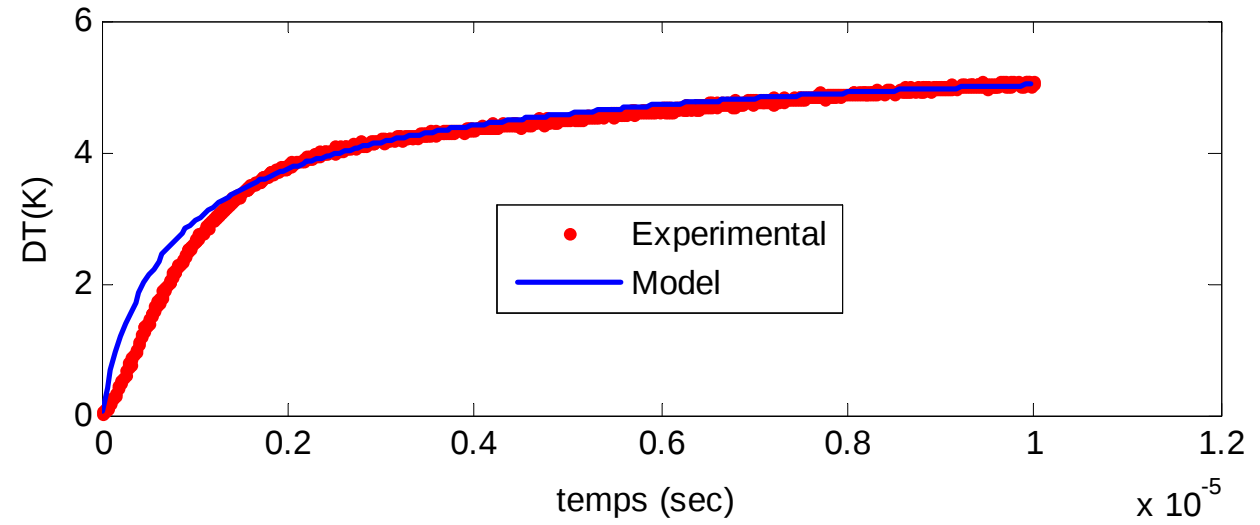


Measurements verification

Measurements

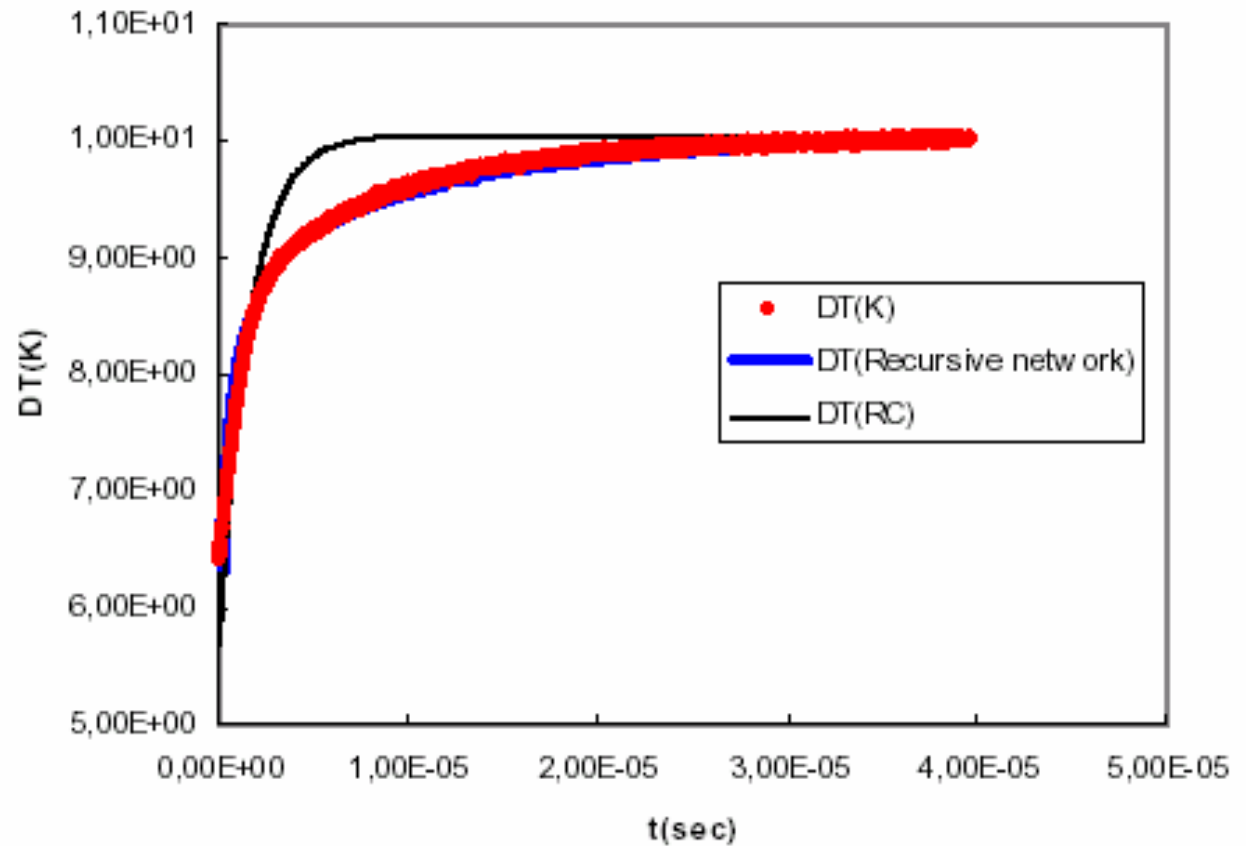
Compact Model

Thermogram –
 $T_j = f(t)$ $A_e = 6,56 \mu\text{m}^2$



Measurements perspectives

Comparison RC Network/Recursive Network



Thermogram –
 $T_j = f(t)$ $A_e = 5 \times 3,16 \mu m^2$

Conclusions

Self Heating at a glance

- Resolution of the heat equation in a real 3D HBT SiGe by a new dynamic model, with short calculations time
- Determination of the thermal network parameters Z_{TH} (R_{TH} and C_{TH}) for standard self heating compact models
- Very good agreement with experimental results
- Simple thermal compact model to describe self heating phenomenon in electrical devices

Some perspectives

Simulation for smaller X_C and Y_C – R_{TH} C_{TH} dependence of (X_C, Y_C) – Coupling phenomena structure

- Developing the thermal model for HBT SOI technology
- Development of the equivalent recursive network





Thanks for your attention

5th European HICUM Workshop
STMicroelectronics Crolles, France June 6-7, 2005