

# Model Status: Agilent EEsof EDA IC-CAP Model Parameter Extractor

Latest release is IC-CAP 2006B, Add-On2 – Apr 2007

Model	Shipping Version	Future Versions
BSIM3	3.3	
BSIM4	4.5	
BSIMSOI	3.2 (from AdMOS)	4.0 (Nov 07) or 4.1 (Apr 08)
PSP	102.0	102.1 (Aug 07) 102.2 (Apr 08)
HiSIM2	1.2 (from AdMOS)	2.31 (Nov 07)
R3		
MEXTRAM	504	
HICUM2	2.22 (from Xmod)	
HICUM L0	1.1 (from Xmod)	

# Model Status: Agilent EEsof EDA ADS/RFDE Circuit Simulator

Latest release is ADS/RFDE 2006A, UR2 June 7, 2007

<b>Model</b>	<b>Shipping Version</b>	<b>Future Version</b>
BSIM3	3.3	
BSIM4	4.5	4.61 (Aug 07)
BSIMSOI	4.0	4.1 (Feb 08)
PSP	102.1	102.2 (Feb 08)
HiSIM2	2.31 (UR2)	
MEXTRAM	504.6.2	
HICUM	2.22	2.23 (Feb 08)
HICUM L0	1.11	1.12 (Aug 07)
MM20	Yes	
Juncap	Yes	
R3		1.0 (Feb 08)
Verilog-A	LRM 2.2	

SiMKit 2.3.5 now ships with ADS/RFDE 2006A; users can also download it from the NXP SiMKit website for ADS/RFDE 2004A and 2005A.

# Model Status: Agilent EEsof EDA GoldenGate Circuit Simulator

Latest release is 4.0.28 (May 2007)

see also <http://wiki.scs.agilent.com/index.php?pagename=Devices>

<b>Model</b>	<b>Shipping Version</b>	<b>Future Version</b>
BSIM3	3.2, 3.3, 3.24	
BSIM4	4.21, 4.3, 4.4, 4.5, 4.60	
BSIMSOI	4.0.0 (3SOI, 4SOI)	
PSP	1.000, 1.01, 1.021	
HiSIM2	1.2, 2	
MEXTRAM	504.2, 503	
HICUM	2,1, 2,21, 2.22	
R2	cmc, cmc_et (?)	
Verilog-A	LRM 2.2 partial	