

Sunday, 08.09.2013			
18:00-20:00		Get Together	
Monday, 09.09.2013			
Room:	Konferenz 1	Konferenz 2	Konferenz 6
09:00-09:30	Welcome Adresses		
09:30-10:30	Plenary Talk: Leon Chua		
10:30-11:00	Coffee Break & Demo/Poster Session (continues during next lecture session) - Room: Konferenz 3 -		
11:00-12:40	Nonlinear Dynamics in Cellular Wave Computing	Analog Circuit Design 1	Sensors & Actuators 1
12:40-14:00	Lunch		
14:00-15:00	Plenary Talk: Theodore Berger		
15:10-16:50	Configurable Neuromorphic Systems	Analog Circuit Design 2	Memristive Circuits
16:50-17:20	Coffee Break		
17:20-19:00	Memristor Technology in Nonlinear Dynamical Circuits	Biomedical Circuits	Sensors & Actuators 2
Tuesday, 10.09.2013			
Room:	Konferenz 1	Konferenz 2	Konferenz 6
09:00-10:00	Plenary Talk: Josef Nossek		
10:00-10:30	Coffee Break & Poster Session (continues during next lecture session) - Room: Konferenz 3 -		
10:30-12:10	Applications of Mathematical Methods in Circuits and Systems Design	Circuit Analysis 1	Analog RF Circuits & Design
12:10-13:40	Lunch		
13:40-14:40	Plenary Talk: Tamas Roska		
14:40-15:10	Coffee Break		
15:10-16:50	Problems and Solutions on Mega Core Array Architectures	Circuit Analysis 2	Digital RF Circuits & Design
18:00-20:00		Optional: Dresden City Tour	
Wednesday, 11.09.2013			
Room:	Konferenz 1	Konferenz 2	Konferenz 6
09:00-10:00	Plenary Talk: Maciej Ogorzalek		
10:00-10:30	Coffee Break		
10:30-12:10	Mixed-signal Circuit Design and Converters 1	Semiconductor Devices and Technology	Oscillators 1
12:10-13:40	Lunch		
13:40-14:40	Plenary Talk: Wolfgang Porod		
14:40-15:20	Award Ceremony (2013 Vitold Belevitch Award and Student Best Paper Award) / Invitation to ECCTD 2015		
15:30-17:10	Mixed-signal Circuit Design and Converters 2	ADC & DAC	Oscillators 2
19:00-23:00		Conference Dinner	
Thursday, 12.09.2013			
Room:	Konferenz 1	Konferenz 2	Konferenz 6
09:00-10:00	Plenary Talk / Tutorial: Albrecht Reibiger		
10:00-10:30	Coffee Break		
10:30-12:10	Digital Circuit Design	Cellular Neural Networks	Filter Design 1
12:10-13:40	Lunch		
13:40-15:20	Signal Processing	Chaos and Bifurcation	Filter Design 2
End of Conference			