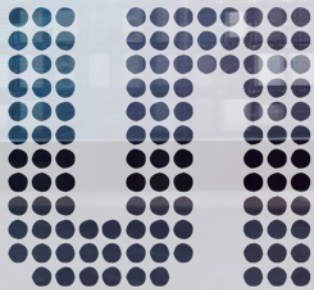




Parasitic Bipolar Modeling for Voltage Scalable MOSFET Transistors



Mingchun Tang

24.10.2014

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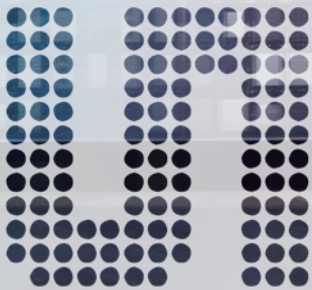


Agenda

PMOS & NMOS

- Device x-section
- Measurement table
- Single geometry model
- Scalable model

Modeling issue





Thank you

Please visit our website www.ams.com