

# Agenda AKB 2016, Infineon München

**Location: Infineon, Neubiberg, Campeon 1-12  
Building 02, Conference Zone, Room 02.1.130**

DAY1 2016-11-24

from	to	Time	Slot	Topic	Speaker	Company	Titel
14:00	14:10	00:10		Opening remarks	Jörg Berkner	Infineon	Introduction
14:10	14:35	00:25		Member matters	Simone Locci	Infineon	New member introduction, next AKB location and date
14:35	15:15	00:40	1	P + D	G. Panagopoulos	Intel	Modelling of the parasitic pnp in standard CMOS technologies: Collector grounded configuration
15:15	15:55	00:40	2	P + D	Brendan OhAnnadh	Analog Dev.	Analog Devices in Ireland, celebrating 40 years
15:55	16:15	00:20		Coffee Break			
16:15	16:55	00:40	3	P + D	Franz Sischka	SisConsult	Reliable Impedance Measurements and Modeling
16:55	17:35	00:40	4	P + D	Andreas Pawlak	TUD	On the experimental determination of fmax
17:35	17:50	00:15	5	Historical Talk	Franz Sischka	SisConsult	A Quick Revue of Historic BJT Performances and Modelings
17:50	18:35	00:45		Historical Archive	Jörg Berkner	Infineon	A journey through the history of Siemens HL and Infineon
20:00	22:00	02:00		Evening Event	Jörg Berkner	Infineon	Dinner and some farewell live music

DAY2 2016-11-25

from	to	Time	Slot	Topic	Speaker	Company	Titel
09:00	09:05	00:05		Opening remarks	Simone Locci		
09:05	09:45	00:40	6	P + D	Tommy Rosenbaum	ST	TCAD setup for an advanced SiGe HBT technology applied to the HS, MV and HV transistor versions
09:45	10:25	00:40	7	P + D	Ehrenfried Seebacher	AMS	TCAD based variability modeling
10:25	10:45	00:20		Coffee Break			
10:45	11:25	00:40	8	P + D	KWP	Infineon	A SPECTRE Sub-Circuit Model for Resistors including Self-Heating
11:25	12:05	00:40	9	P + D	Gerhard Fischer	IHP	Temperature dependence of BT relevant sheet resistances
12:05	13:05	01:00		Lunch			
13:05	13:20	00:15	10	Lunch talk	Jörg Berkner	Infineon	Chipart - immortalized in Silicon
13:20	14:00	00:40	11	P + D	Thomas Zimmer	IMS	BEOL-investigation on selfheating and SOA of SiGe HBT'
14:00	14:40	00:40	12	P + D	Dedier Celi	ST	Extraction of parasitic PNP substrate parameters of NPN devices without access to the substrate terminal
14:40	14:50	00:10		Adjorn			

P+D=Presentation+Diskussion

Version 2016-10-25

Titels may be preliminary and subject to change